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NOTICE OF ALLOWANCE  
AND ISSUE FEE DUE

All communications must be directed to the Examiner.

The term of allowance is from the applicant's date of filing.

SEARCHED  INDEXED  TOTAL CLAIMS  EXAMINER AND GROUP ART UNIT  DATE MAILED

67/343,777 04/20/89 015 KAI, H 254 01/24/91

PATENT

NAME: NAKADA

HIROSHI

TYPE OF

INVENTION: LIQUID CRYSTAL ACTIVE-MATRIX DISPLAY DEVICE

SEARCHED  INDEXED  TOTAL CLAIMS  EXAMINER AND GROUP ART UNIT  DATE MAILED

62 - 2071552 350-333.000 M20 UTILITY NO \$1050.00 04/24/91

**THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT.  
PROSECUTION ON THE MERITS IS CLOSED.**

**THE ISSUE FEE MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS  
APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED.**

**HOW TO RESPOND TO THIS NOTICE:**

I. Review the SMALL ENTITY Status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

- A. If the Status is changed, pay twice the amount of the FEE DUE shown above and notify the Patent and Trademark Office of the change in status, or
- B. If the Status is the same, pay the FEE DUE shown above.

II. Part B of this notice should be completed and returned to the Patent and Trademark Office (PTO) with your ISSUE FEE. Even if the ISSUE FEE has already been paid by a charge to deposit account, Part B should be completed and returned. If you are charging the ISSUE FEE to your deposit account, Part C of this notice should also be completed and returned.

III. All communications regarding this application must give series code (or filing date), serial number and batch number. Please direct all communications prior to issuance to Box ISSUE FEE unless advised to the contrary.

If the SMALL ENTITY is shown as NO:

- A. Pay FEE DUE shown above, or
- B. File verified statement of Small Entity Status before, or with, payment of 1/2 the FEE DUE shown above.

***IMPORTANT REMINDER: Patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees.***



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SERIAL NUMBER	FILED DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NO.
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EXAMINER

ART UNIT	PAPER NUMBER
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15

DATE MAILED:

NOTICE OF ALLOWABILITY

PART I.

- This communication is responsive to the information disclosure statement filed July 16, 1990.
- All the claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice Of Allowance And Issue Fee Due or other appropriate communication will be sent in due course.
- The allowed claims are 6-20 ; claims 1-5 have been cancelled.
- The drawings filed on July 19, 1990 are acceptable.
- Acknowledgment is made of the claim for priority under 35 U.S.C. 119. The certified copy has  been received.  not been received.  been filed in parent application Serial No. \_\_\_\_\_, filed on \_\_\_\_\_.
- Note the attached Examiner's Amendment.
- Note the attached Examiner Interview Summary Record, PTOL-413.
- Note the attached Examiner's Statement of Reasons for Allowance.
- Note the attached NOTICE OF REFERENCES CITED, PTO-892.
- Note the attached INFORMATION DISCLOSURE CITATION, PTO-1449.

PART II.

A SHORTENED STATUTORY PERIOD FOR RESPONSE to comply with the requirements noted below is set to EXPIRE THREE MONTHS FROM THE "DATE MAILED" indicated on this form. Failure to timely comply will result in the ABANDONMENT of this application. Extensions of time may be obtained under the provisions of 37 CFR 1.136(a).

- Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL APPLICATION, PTO-152, which discloses that the oath or declaration is deficient. A SUBSTITUTE OATH OR DECLARATION IS REQUIRED.
- APPLICANT MUST MAKE THE DRAWING CHANGES INDICATED BELOW IN THE MANNER SET FORTH ON THE REVERSE SIDE OF THIS PAPER.
  - Drawing informalities are indicated on the NOTICE RE PATENT DRAWINGS, PTO-948, attached hereto or to Paper No. \_\_\_\_\_, CORRECTION IS REQUIRED.
  - The proposed drawing correction filed on \_\_\_\_\_ has been approved by the examiner. CORRECTION IS REQUIRED.
  - Approved drawing corrections are described by the examiner in the attached EXAMINER'S AMENDMENT. CORRECTION IS REQUIRED.
  - Formal drawings are now REQUIRED.

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Any response to this letter should include in the upper right hand corner, the following information from the NOTICE OF ALLOWANCE AND ISSUE FEE DUE: ISSUE BATCH NUMBER, DATE OF THE NOTICE OF ALLOWANCE, AND SERIAL NUMBER.

Attachments:

<input type="checkbox"/> Examiner's Amendment	<input type="checkbox"/> Notice of Informal Application, PTO-152
<input type="checkbox"/> Examiner Interview Summary Record, PTO-413	<input type="checkbox"/> Notice re Patent Drawings, PTO-948
<input checked="" type="checkbox"/> Reasons for Allowance	<input type="checkbox"/> Listing of Bonded Draftsmen
<input type="checkbox"/> Notice of References Cited, PTO-892	<input type="checkbox"/> Other
<input checked="" type="checkbox"/> Information Disclosure Citation, PTO-1449	

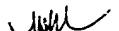
Art Unit 254

The following is an Examiner's Statement of Reasons for Allowance: Misawa teaches in Figs. 1-4 a MOS capacitor 41 connected from the drain of the thin film transistor 39 to the adjacent gate line 44 wherein the structure of the MOS capacitor is the same as that of the thin film transistor. Okubo teaches in Figs 3-4 the drain D<sub>1</sub> overlapped a portion P of the gate line G<sub>1</sub>. Okubo also teaches that "it is difficult to increase the effective display area since the display cannot be provided on the gate lines" (column 3, lines 7-9). Therefore, the limitations of claims 6-20 are unobvious over Misawa and Okubo.

Any comments considered necessary by applicant must be submitted no later than the payment of the Issue Fee and, to avoid processing delays, should preferably accompany the Issue Fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication should be directed to Examiner Huy K. Mai at telephone number (703) 308-1287.

  
STANLEY D. MILLER  
SPECIALIST IN ELECTRONIC  
CIRCUITS AND SYSTEMS

  
Mai/rk  
January 15, 1991